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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Daniel R. Knebel, et al.

Examiner: Ferris

Serial No.: 09/406,663

Art Unit: 2123

Filed: September 27, 1999

Docket: YOR919990438US1 (13031)

For: SYSTEM AND METHOD FOR  
VLSI VISUALIZATIONCommissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450DECLARATION UNDER 37 C.F.R. §1.312

Dear Sir:

We, Daniel R. Knebel, Mark A. Lavin, J. Moreno, Stanislav Polonsky, Pia N. Sanda and Steven H. Voldman, Declare that:

1. We are the co-inventors of the subject matter disclosed and claimed in the above-identified patent application, and we are submitting this Declaration in response to the Office Action dated March 12, 2003 issued in the application.
2. In the Office Action, the Examiner rejected Claims 1-7 and 10-42 as being fully anticipated by an article "Diagnosis and Characterization of Timing-Related Defects by Time-Dependent Light Emission." A copy of this article is submitted herewith as Exhibit A.
3. We are familiar with the preparation and contents of this article.
4. This article describes the work we did and that was subsequently disclosed and claimed in the above-identified patent application.

We hereby declare that all statements made herein of our own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United

States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

June 11, 2003

Dated

Daniel R. Knebel

Daniel R. Knebel

27 OCT 2003

Dated

Mark A. Lavin

Mark A. Lavin

June 26, 2003

Dated

J. Moreno

J. Moreno

June 11, 2003

Dated

Stanislav Polonsky

Stanislav Polonsky

Oct. 27, 2003

Dated

Pia N. Sanda

Pia N. Sanda

November 18<sup>th</sup> 2003

Dated

Steven H. Voldman

Steven H. Voldman